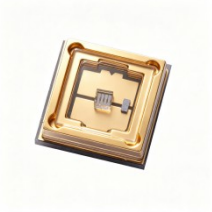
HK255S35CG-100Z



Specification Datasheet

CFCL Optoelectronics (Shenzhen) Co., Ltd.

Model No.: HK255S35CG-100Z

Product: 3535 patented package UVC LED (20 × 20 mil chip)

Revision: V1.0

Date: October 2026

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1. General Description

The HK255S35CG-100Z is a high-performance deep-ultraviolet (UVC) LED in a patented 3535 (3.5 × 3.5 mm) SMD package with a sapphire plano lens, integrating a 20 × 20 mil high-efficiency UVC LED chip. It delivers stable ultraviolet output in the 250–260 nm range, with excellent thermal dissipation and a long operating lifetime.

This device is fully compatible with standard surface mount technology (SMT) assembly processes, and is suitable for various sterilization and disinfection applications. All products undergo 100% testing and sorting by forward voltage, optical power and wavelength before delivery.

2. Key Features

- Peak wavelength range: 250.0 – 260.0 nm
- 20 × 20 mil UVC LED chip
- Patented 3535 SMD package with integrated sapphire plano lens
- Package dimensions: 3.50 × 3.50 × 1.27 mm (Length × Width × Height)
- Gold (Au) positive and negative electrodes for high reliability
- Low thermal resistance ceramic substrate
- RoHS, CE and REACH compliant
- 100% visual and electrical inspection before shipment

3. Application Fields

- Drinking water sterilization systems
- Air purifiers and HVAC disinfection units
- Surface disinfection equipment
- Medical device sterilization
- Portable UV sanitizers
- Biological analysis instruments
- UV curing systems
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4. Absolute Maximum Ratings

(Test condition: Ambient temperature Ta = 25°C)

Parameter	Symbol	Rating	Unit
Forward Current	I_F	100	mA
Peak Forward Current (10% duty cycle, 1ms pulse)	$I_{F(peak)}$	150	mA
Reverse Voltage	V_R	5	V
Operating Temperature Range	T_{opr}	-40 ~ +85	°C
Storage Temperature Range	T_{stg}	-40 ~ +100	°C
Maximum Junction Temperature	T_j	105	°C

5. Electro-Optical Characteristics

(Test condition: Ta = 25°C, Forward current I_F = 100 mA)

Parameter	Symbol	Min.	Typ.	Max.	Unit
Peak Wavelength	λ_p	250.0	255.0	260.0	nm
Spectral Half-Width	Δλ	-	12	-	nm
Forward Voltage	V_F	4.8	5.4	6.0	V
Radiant Output Power	P_e	8.0	12.0	16.0	mW
Viewing Angle (Half Intensity)	$2\theta_{1/2}$	-	120	-	deg
Reverse Leakage Current	I_R	-	-	10	μA
Thermal Resistance (Junction to Board)	$R_{th(j-b)}$	-	18	-	°C/W

6. Binning Specifications

(Test condition: Ta = 25°C, I_F = 100 mA)

6.1 Forward Voltage Binning

Level	A01	A02	A03	A04	A05	A06
Min. (V)	4.8	5.0	5.2	5.4	5.6	5.8
Max. (V)	5.0	5.2	5.4	5.6	5.8	6.0

6.2 Radiometric Power Binning

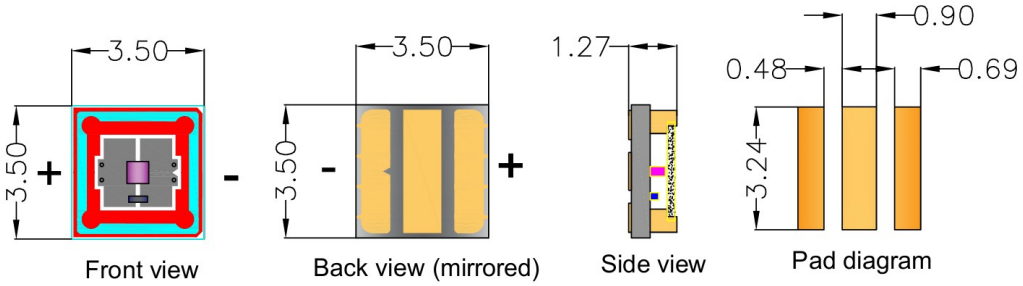
Level	P1	P2	P3	P4
Min.	8.0 mW	10.0 mW	12.0 mW	14.0 mW
Max.	10.0 mW	12.0 mW	14.0 mW	16.0 mW

6.3 Peak Wavelength Binning

Level	255A	255B
Min.	250.0 nm	255.0 nm
Max.	255.0 nm	260.0 nm

7. Outline Dimensions

- **Package Type: 3535 patented package (SMD, sapphire plano lens)**
- **Overall Dimensions:** 3.50 ± 0.10 mm (Length) × 3.50 ± 0.10 mm (Width) × 1.27 ± 0.10 mm (Height)
- **Lens Diameter:** 2.60 mm (Sapphire Plano Lens)
- **Pad Layout:** Standard 3535 SMD footprint



8. Packaging & Storage

8.1 Packaging

- Packaging form: Tape and reel
- Quantity per reel: 1000 pieces
- Reel diameter: 178 mm (7 inch)
- Moisture Sensitivity Level: MSL 3

8.2 Storage Conditions

- Store in a dry environment at room temperature
- Recommended storage: 5°C ~ 30°C, 30% ~ 70% RH
- Avoid direct sunlight, corrosive gases and high humidity
- Reseal the bag properly after opening to prevent moisture absorption

9. Notes & Precautions

1. Peak wavelength tolerance: ± 3.5 nm
2. Radiant flux measurement tolerance: ± 10%
3. Forward voltage tolerance: ± 10%
4. All parameters are measured at chip level by UV-CHIPS
5. UVC LEDs emit high-energy ultraviolet radiation. Avoid direct exposure to eyes or skin during operation.
6. The device is sensitive to electrostatic discharge (ESD). Proper ESD protection is required during handling and assembly.
7. Operating beyond absolute maximum ratings may cause permanent damage to the device.

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