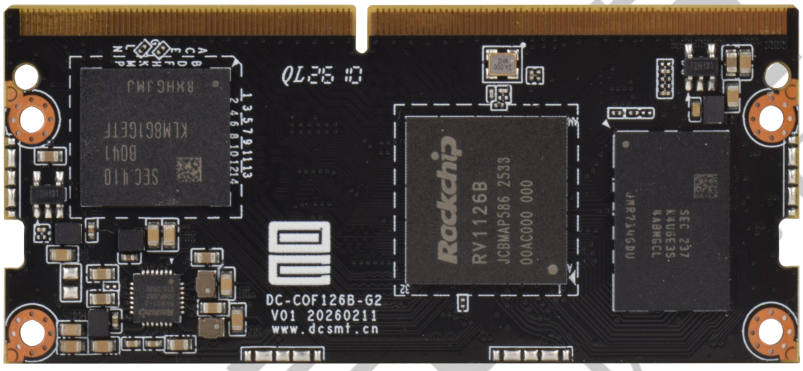
DC-COF126B-G2-V01

产品规格书



版本	日期	变更内容
V1.0	2026-4-18	初版

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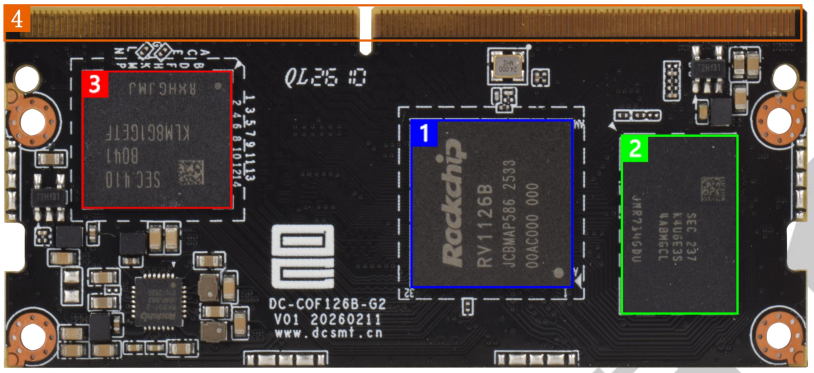
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第一章 产品概述

1.1、概述

DC-COF126B，采用瑞芯微新一代 64 位处理器 RV1126B，基于四核 ARM Cortex-A53 64 位内核，集成了 NEON 和 FPU；内置 NPU 支持 INT8/INT16 混合操作和计算能力高达 3.0TOP；嵌入的视频编码器支持 H.265/H.264 视频编码，也支持多流编码，借助此功能，来自摄像机的视频可以以更高的分辨率编码并存储在本地存储器中，同时将另一个较低分辨率的视频传输到云存储；支持 5G/4G 等高速无线网络通讯；拥有丰富的接口扩展，支持多种视频输入输出接口，可适用于智能 NVR、云终端、物联网网关、工业控制等场景。

第二章 外观示意图

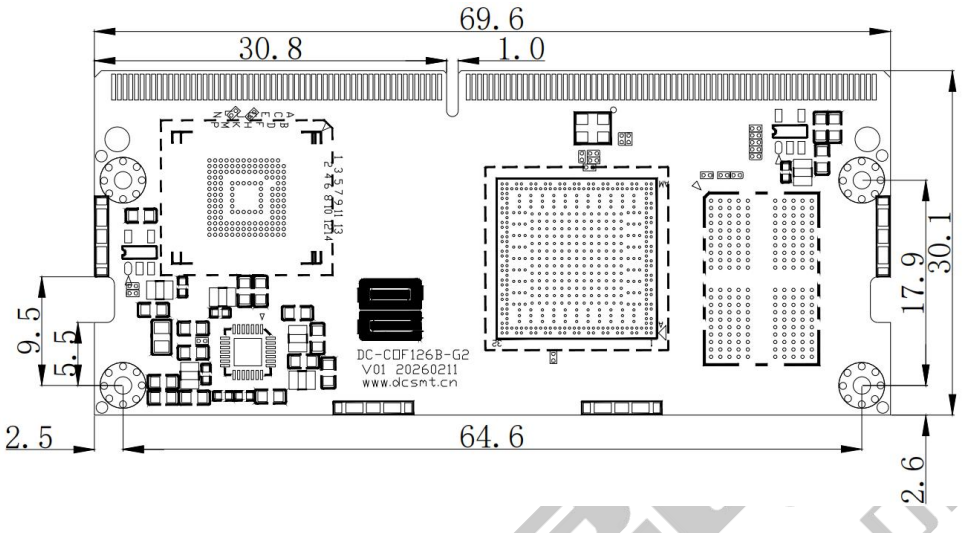


1 RV1126B　2 LPDDR4X　3 eMMC　4 SO-DIMM(标准260P接口)

第三章 主板规格

3.1、PCB 尺寸图

单位：mm

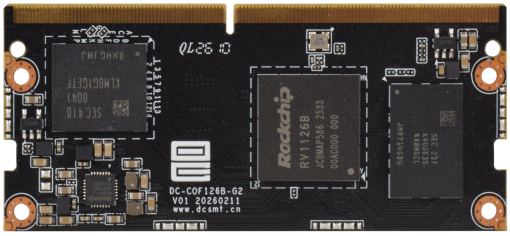


3.2、规格参数

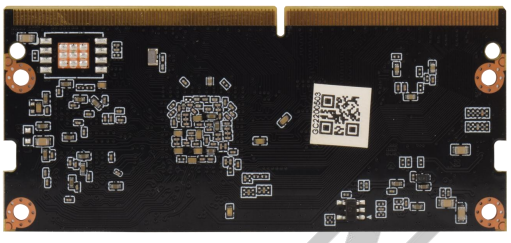
主板尺寸： 69.6*30.1*4mm
主板高度： 正面≤1.5mm，背面≤2mm
PCB 层数： 8 层
PCB 尺寸： 69.6*30.1*1.2mm
PCB 颜色： 黑色
PCB 工艺： 沉金

第四章 引脚定义

259、257、255、253、.....7、5、3、1



2、4、6、8.....254、256、258、260



Pin	Net Name	Pad type	IO Pull	Default function description	IO Power domain
1	VCC5V0_SYS	P		Input Voltage 4.8V-5.5V	5.0V
3	VCC5V0_SYS	P		Input Voltage 4.8V-5.5V	5.0V
5	VCC5V0_SYS	P		Input Voltage 4.8V-5.5V	5.0V
7	VCC5V0_SYS	P		Input Voltage 4.8V-5.5V	5.0V
9	GND	G		GND	
11	GND	G		GND	
13	GND	G		GND	
15	GND	G		GND	
17	GND	G		GND	
19	VCC_LDO1	P		3.3V Output For LDO1	3.3V
21	VCC_3V3	P		3.3V Output For codec,VCCIO_ACODEC Total	3.3V
23	VCC_3V3	P		3.3V Output For codec,VCCIO_ACODEC Total	3.3V
25	VCC_3V3	P		3.3V Output For codec,VCCIO_ACODEC Total	3.3V
27	VCC3V3_SYS	P		3.3V Output ,VCC_3V3 Total Max:800mA	3.3V
29	VCC3V3_SYS	P		3.3V Output ,VCC_3V3 Total Max:800mA	3.3V
31	VCC3V3_SYS	P		3.3V Output ,VCC_3V3 Total Max:800mA	3.3V
33	VCC_1V8	P		1.8V Output ,VCC_1V8 Total Max:500mA	1.8V
35	VCC_1V8	P		1.8V Output ,VCC_1V8 Total Max:500mA	1.8V
37	VCC_1V8	P		1.8V Output ,VCC_1V8 Total Max:500mA	1.8V
39	GND	G		GND	
41	USB_OTG_DM	I/O		USB_OTG_DM	3.3V
43	USB_OTG_DP	I/O		USB_OTG_DP	3.3V
45	GND	G		GND	
47	USB_HOST_DM	I/O		USB_HOST_DM	3.3V
49	USB_HOST_DP	I/O		USB_HOST_DP	3.3V
51	GND	G		GND	
53	USB3_SSTXP	I/O		USB3_SSTXP	3.3V
55	USB3_SSTXN	I/O		USB3_SSTXN	3.3V
57	GND	G		GND	
59	USB3_SSRXP	I/O		USB3_SSRXP	3.3V
61	USB3_SSRXN	I/O		USB3_SSRXN	3.3V
63	GND	G		GND	
65	UART0_RX_DBG/PWM1_CH3_M0/I2C1_SDA_M0/GPIO0_B4_U	I		UART0 serial data input	3.3V

67	SDMMC0_DET/PWM1_CH0_M0/GPIO0_A5_U	I/O		Detection signal of SDMMC0 card slot	3.3V
69	UART0_TX_DBG/PWM1_CH2_M0/I2C1_SCL_M0/GPIO0_B3_U	O		UART0 serial data output	3.3V
71	FSPI1_D3_M0/PWM1_CH1_M0/GPIO0_A1_Z			NC	3.3V
73	CLK_32K/RTC_32K_OUT/GPIO0_A2_Z	I/O		WiFi clock output	3.3V
75	REF_CLK0_OUT/TEST_CLK0_OUT/GPIO0_A0_Z			NC	3.3V
77	GND	G		GND	
79	UART2_RTSN_M1/PDM_SDI3_M0/SAI0_SDI3_M0/GPIO7_A7_D	I		SPK_EN ,Active H	3.3V
81	UART2_RX_M1/I2C1_SCL_M3/PDM_SDI2_M0/GPIO7_B0_D	I/O		I2C1_SCL_M3	3.3V
83	PDM_SDI0_M0/SAI0_SDI0_M0/GPIO7_A6_D	I/O		SAI0_SDI0_M0	3.3V
85	UART2_CTSN_M1/I2C4_SDA_M3/PDM_CLK0_M0/GPIO7_A4_D	I		Detection signal for headphone insertion	3.3V
87	VCCIO_SD	P		3.3V Output,TF Card Power,VCC3V3_SD Total Max:100mA	3.3V
89	SDMMC0_CMD/UART3_CTSN_M0/UART4_TX_M3/GPIO2_A5_D	I/O		SDMMC0 card command output and response input	3.3V
91	SDMMC0_D3/UART3_TX_M0/UART4_CTSN_M3/GPIO2_A3_D	I/O		SDMMC0 card data input and output	3.3V
93	SDMMC0_D2/UART3_RX_M0/UART4_RTSN_M3/GPIO2_A2_D	I/O		SDMMC0 card data input and output	3.3V
95	GND	G		GND	
97	MIC0_P	I		Microphone0 input positive pole	3.3V
99	MIC0_N	I		Microphone0 input negative pole	3.3V
101	GND	G		GND	
103	MIC1_P	I		Microphone1 input positive pole	3.3V
105	MIC1_N	I		Microphone1 input negative pole	3.3V
107	GND	G		GND	
109	MIPI_CSI_RX0_D2P	I		MIPI_CSI_RX0_D2P	1.8V
111	MIPI_CSI_RX0_D2N	I		MIPI_CSI_RX0_D2N	1.8V
113	GND	G		GND	
115	MIPI_CSI_RX0_D3P	I		MIPI_CSI_RX0_D3P	1.8V
117	MIPI_CSI_RX0_D3N	I		MIPI_CSI_RX0_D3N	1.8V
119	GND	G		GND	
121	MIPI_CSI_RX0_CLK1P	I		MIPI_CSI_RX0_CLK1P	1.8V
123	MIPI_CSI_RX0_CLK1N	I		MIPI_CSI_RX0_CLK1N	1.8V
125	GND	G		GND	
127	MIPI_CSI_RX0_CLK0P	I		MIPI_CSI_RX0_CLK0P	1.8V
129	MIPI_CSI_RX0_CLK0N	I		MIPI_CSI_RX0_CLK0N	1.8V
131	GND	G		GND	
133	MIPI_CSI_RX0_D1P	I		MIPI_CSI_RX0_D1P	1.8V
135	MIPI_CSI_RX0_D1N	I		MIPI_CSI_RX0_D1N	1.8V
137	GND	G		GND	
139	MIPI_CSI_RX0_D0P	I		MIPI_CSI_RX0_D0P	1.8V

141	MIPI_CSI_RX0_DON	I		MIPI_CSI_RX0_DON	1.8V
143	GND	G		GND	
145	GND	G		GND	
147	MIPI_CSI_RX1_D2P	I		MIPI_CSI_RX1_D2P	1.8V
149	MIPI_CSI_RX1_D2N	I		MIPI_CSI_RX1_D2N	1.8V
151	GND	G		GND	
153	MIPI_CSI_RX1_D3P	I		MIPI_CSI_RX1_D3P	1.8V
155	MIPI_CSI_RX1_D3N	I		MIPI_CSI_RX1_D3N	1.8V
157	GND	G		GND	
159	MIPI_CSI_RX1_CLK1P	I		MIPI_CSI_RX1_CLK1P	1.8V
161	MIPI_CSI_RX1_CLK1N	I		MIPI_CSI_RX1_CLK1N	1.8V
163	GND	G		GND	
165	MIPI_CSI_RX1_CLK0P	I		MIPI_CSI_RX1_CLK0P	1.8V
167	MIPI_CSI_RX1_CLK0N	I		MIPI_CSI_RX1_CLK0N	1.8V
169	GND	G		GND	
171	MIPI_CSI_RX1_D1P	I		MIPI_CSI_RX1_D1P	1.8V
173	MIPI_CSI_RX1_D1N	I		MIPI_CSI_RX1_D1N	1.8V
175	GND	G		GND	
177	MIPI_CSI_RX1_D0P	I		MIPI_CSI_RX1_D0P	1.8V
179	MIPI_CSI_RX1_D0N	I		MIPI_CSI_RX1_D0N	1.8V
181	GND	G		GND	
183	MIPI_DSI_D3P	O		MIPI_DSI_D3P	1.8V
185	MIPI_DSI_D3N	O		MIPI_DSI_D3N	1.8V
187	GND	G		GND	
189	MIPI_DSI_D2P	O		MIPI_DSI_D2P	1.8V
191	MIPI_DSI_D2N	O		MIPI_DSI_D2N	1.8V
193	GND	G		GND	
195	MIPI_DSI_CLKP	O		MIPI_DSI_CLKP	1.8V
197	MIPI_DSI_CLKN	O		MIPI_DSI_CLKN	1.8V
199	GND	G		GND	
201	MIPI_DSI_D1P	O		MIPI_DSI_D1P	1.8V
203	MIPI_DSI_D1N	O		MIPI_DSI_D1N	1.8V
205	GND	G		GND	
207	MIPI_DSI_D0P	O		MIPI_DSI_D0P	1.8V
209	MIPI_DSI_D0N	O		MIPI_DSI_D0N	1.8V
211	GND	G		GND	
213	FEPHY_TXP	O		FEPHY_TXP	1.8V
215	FEPHY_TXN	O		FEPHY_TXN	1.8V
217	GND	G		GND	
219	FEPHY_RXP	I		FEPHY_RXP	1.8V
221	FEPHY_RXN	I		FEPHY_RXN	1.8V
223	GND	G		GND	
225	VO_LCDC_D20/PWM3_CH4_M1/GPIO5_C4_D	I/O		LCDC RGB interface data output, MCU interfacedata input/output	1.8V
227	VO_LCDC_D19/PWM3_CH3_M1/GPIO5_C3_D	I/O		LCDC RGB interface data output, MCU interfacedata input/output	1.8V
229	VO_LCDC_VSYNC/SPI1_MOSI_M2/PWM1_CH1_M1/GPIO5_D2_D	O		LCDC RGB interface vertical sync pulse,MCU interface CSN signal	1.8V
231	VO_LCDC_DEN/SPI1_CSN0_M2/PWM0_CH6_M2/GPIO5_D0_D	O		LCDC RGB interface data enable,MCU interface REN signal	1.8V

233	SDMMC1_D0/I2C1_SCL_M1/GPIO3_A2_D	I/O		SDMMC1 card data input and output	3.3V
235	SDMMC1_CLK/GPIO3_A0_D	I/O		SDMMC1 card clock signal	3.3V
237	SDMMC1_CMD/GPIO3_A1_D	I/O		SDMMC1 card command output and response input	3.3V
239	SDMMC1_D1/GPIO3_A3_D	I/O		SDMMC1 card data input and output	3.3V
241	SDMMC1_D2/GPIO3_A4_D	I/O		SDMMC1 card data input and output	3.3V
243	SDMMC1_D3/GPIO3_A5_D	I/O		SDMMC1 card data input and output	3.3V
245	UART1_RX_M1/SAI2_SDI1_M0/I2C5_SDA_M1/GPIO3_B7_D			BT_WAKE_HOST	3.3V
247	UART2_TX_BT/SAI2_SDI2_M0/GPIO3_B1_D	O		UART2_TX_BT	3.3V
249	UART1_TX_M1/SAI2_MCLK_M0/I2C5_SCL_M1/GPIO3_B6_D			HOST_WAKE_BT	3.3V
251	UART2_RTSN_BT/GPIO3_A6_D	I/O		UART2_RTSN_BT	3.3V
253	UART2_CTSN_BT/GPIO3_A7_D	I/O		UART2_CTSN_BT	3.3V
255	UART2_RX_BT/GPIO3_B0_D	O		UART2_RX_BT	3.3V
257	PMIC_PWRON	I/O		PMIC PWRON_KEY Input	3.3V
259	GND	G		GND	
2	VCC5V0_SYS	P		Input Voltage 4.8V-5.5V	5.0V
4	VCC5V0_SYS	P		Input Voltage 4.8V-5.5V	5.0V
6	VCC5V0_SYS	P		Input Voltage 4.8V-5.5V	5.0V
8	VCC5V0_SYS	P		Input Voltage 4.8V-5.5V	5.0V
10	GND	G		GND	5.0V
12	GND	G		GND	
14	GND	G		GND	
16	GND	G		GND	
18	GND	G		GND	
20	VCC_LDO1	P		3.3V Output For LDO1	3.3V
22	VCC_3V3	P		3.3V Output For codec,VCCIO_ACODEC Total	3.3V
24	VCC_3V3	P		3.3V Output For codec,VCCIO_ACODEC Total	3.3V
26	VCC_3V3	P		3.3V Output For codec,VCCIO_ACODEC Total	3.3V
28	VCC3V3_SYS	P		3.3V Output ,VCC_3V3 Total Max:800mA	3.3V
30	VCC3V3_SYS	P		3.3V Output ,VCC_3V3 Total Max:800mA	3.3V
32	VCC3V3_SYS	P		3.3V Output ,VCC_3V3 Total Max:800mA	3.3V
34	VCC_1V8	P		1.8V Output ,VCC_1V8 Total Max:500mA	1.8V
36	VCC_1V8	P		1.8V Output ,VCC_1V8 Total Max:500mA	1.8V
38	VCC_1V8	P		1.8V Output ,VCC_1V8 Total Max:500mA	1.8V
40	GND	G		GND	
42	VCC_RTC	P		3.3V Output For RTC	3.3V
44	GND	G		GND	
46	USB_OTG_VBUSDET	I		Prot insert detect when act as USB device	3.3V
48	USB_DRD_ID			NC	3.3V
50	GND	G		GND	
52	PWR_CTRL0/GPIO0_A3_U	I/O		BT_REG_ON_3V3	3.3V
54	FSPI1_CLK_M0/SPI0_CLK_M0/GPIO0_B2_D	I/O		WIFI_REG_ON_3V3	3.3V
56	FSPI1_D1_M0/SPI0_MISO_M0/GPIO0_B1_D	I/O		WIFI_WAKE_HOST_3V3	3.3V
58	GND	G		GND	
60	FSPI1_D0_M0/SPI0_MOSI_M0/GP	I/O		MIPI DSI TP_Reset ,Active L	3.3V

	IO0_B0_D				
62	FSPI1_CSN0_M0/SPI0_CSON_M0/GPIO0_A7_U	I/O		MIPI DSI TP_INT ,Active L	3.3V
64	GND	G		GND	
66	FSPI1_D2_M0/SPI0_CS1N_M0/GPIO0_A6_U	I/O		MIPI DSI Power EN ,Active H	3.3V
68	PWR_CTRL1/GPIO0_A4_D	I/O		MIPI DSI Reset, Active L	
70	I2C2_SDA_M0/PWM0_CH5_M0/GPIO0_D1_D	I/O		I2C2_SDA_M0 for TP	3.3V
72	I2C2_SCL_M0/PWM0_CH4_M0/GPIO0_D0_D	I/O		I2C2_SCL_M0 for TP	3.3V
74	SPI2AHB_D0/PWM0_CH3_M0/GPIO0_C7_D	I/O		PWM4:LVDS_panel backlight brightness control output	3.3V
76	SPI2AHB_D2/PWM0_CH1_M0/GPIO0_C5_D	I		IR Input	3.3V
78	SPI2AHB_D1/PWM0_CH2_M0/GPIO0_C6_D	I/O		WORKING_LEDEN	3.3V
80	GND	G		GND	
82	UART2_TX_M1/I2C1_SDA_M3/PDM_SDI1_M0/GPIO7_B1_D	I/O		I2C1_SDA_M3	3.3V
84	PWM2_CH6_M1/SAI0_MCLK_M0/GPIO7_A2_D	I/O		SAI0_MCLK_M0	3.3V
86	SDMMC0_CLUART3_RTSN_M0/UART4_RX_M3/GPIO2_A4_D	I/O		SDMMC0 card clock signal	3.3V
88	SDMMC0_D0/UART0_RX_M0/I2C0_SDA_M1/GPIO2_A0_D	I/O		SDMMC0 card data input and output	3.3V
90	SDMMC0_D1/UART0_TX_M0/I2C0_SCL_M1/GPIO2_A1_D	I/O		SDMMC0 card data input and output	3.3V
92	DSM_AUD_LP/SAI0_SDO0_M0/GPIO7_A5_D	O		SAI0_SDO0_M0	3.3V
94	PWM2_CH7_M1/SAI0_LRCK_M0/GPIO7_A3_D	I/O		SAI0_LRCK_M0	3.3V
96	PWM2_CH4_M1/SAI0_SCLK_M0/GPIO7_A0_D	I/O		SAI0_SCLK_M0	3.3V
98	PWM2_CH5_M1/I2C4_SCL_M3/PDM_CLK1_M0/GPIO7_A1_D	I/O		Headphone insertion detection	3.3V
100	UART4_RX_M0/GPIO4_A2_D	I		MIPI_RX0_PDN	
102	UART4_TX_M0/SAI1_MCLK_M1/GPIO4_A3_D	I		MIPI_RX1_PDN	
104	I2C3_SCL_CAM/SAI1_SCLK_M1/GPIO4_A4_D	I/O		I2C3_SCL_CAM	3.3V
106	I2C4_SDA_M2/UART5_TX_M0/SAI1_SDI_M1 I/GPIO4_A6_D	I/O		I2C4_SDA_CAM	3.3V
108	GND	G		GND	
110	I2C4_SCL_CAM/UART5_RX_M0/SAI1_SDO_M1/GPIO4_A7_D	I/O		I2C4_SCL_CAM	3.3V
112	UART4_RTSN_M0/I2C1_SDA_M2/CAM_CLK3_OUT/GPIO4_A0_U	I		MIPI_RX0_RST	1.8V
114	UART4_CTSN_M/I2C1_SCL_M2/CAM_CLK2_OUT/GPIO4_A1_U	I		MIPI_RX1_RST	1.8V
116	I2C3_SDA_CAM/SAI1_LRCK_M1/G	I/O		I2C3_SDA_CAM	3.3V

	PIO4_A5_D				
118	UART5_RTSN_M0/CAM_CLK1_OUT/GPIO4_B0_D	I/O		MIPI_MCLK_OUT1	3.3V
120	GND/	G		GND	
122	UART5_CTSN_M0/CAM_CLK0_OUT/GPIO4_B1_D	I/O		MIPI_MCLK_OUT0	3.3V
124	VCCIO4	P		1.8V Output ,VCC_1V8 Total Max:500mA	1.8V
126	VCCIO6	P		3.3V Output For codec,VCCIO_ACODEC Total	3.3V
128	PWM1_CH0_M2/UART4_TX_M2/I2C3_SCL_M3/GPIO6_A0_D	O		UART4_TX_M2	3.3V
130	UART6_RX_M1/SPI1_CSN0_M0/SAIO_SDI1_M1/GPIO6_B1_D	I		UART6_RX_M1	3.3V
132	UART6_RTSN_M1/SPI1_MOSI_M0/PDM_SDI2_M1/GPIO6_B2_D	I/O		CAM_SYNC0	1.8V
134	UART6_CTSN_M1/SPI1_MISO_M0/PDM_SDI3_M1/GPIO6_B3_D	I/O		CAM_SYNC1	1.8V
136	UART7_TX_M1/SPI1_CLK_M0/PDM_CLK0_M1/GPIO6_B4_D	O		UART7_TX_M1	3.3V
138	UART7_RX_M1/PDM_SDI0_M1/GPIO6_B5_D	I		UART7_RX_M1	3.3V
140	UART7_RTSN_M1/PDM_SDI1_M1/GPIO6_B6_D	I/O		MIPI_CAM3_PWREN_H	3.3V
142	UART7_CTSN_M1/PDM_CLK1_M1/GPIO6_B7_D	I/O		MIPI_CAM4_PWREN_H	3.3V
144	GND	G		GND	
146	GND	G		GND	
148	PWM1_CH1_M2/UART4_RX_M2/I2C3_SDA_M3/GPIO6_A1_D	I		UART4_RX_M2	3.3V
150	PWM1_CH2_M2/UART5_TX_M2/I2C4_SCL_M1/GPIO6_A2_D	O		UART5_TX_M2	3.3V
152	PWM1_CH3_M2/UART5_RX_M2/I2C4_SDA_M1/GPIO6_A3_D	I		UART5_RX_M2	3.3V
154	PWM2_CH0_M2/UART5_RTSN_M2/I2C5_SCL_M3/GPIO6_A4_D	I/O		I2C5_SCL_M3	3.3V
156	PWM2_CH1_M2/UART5_CTSN_M2/I2C5_SDA_M3/GPIO6_A5_D	I/O		I2C5_SDA_M3	3.3V
158	PWM2_CH2_M2/UART4_RTSN_M2/GPIO6_A6_D	I/O		LCS_RST	3.3V
160	PWM2_CH3_M2/UART4_CTSN_M2/SAIO_SDI3_M1/GPIO6_A7_D	I/O		USB_PWR_EN1	3.3V
162	UART6_TX_M1/SPI1_CSN1_M0/SAIO_SDI2_M1/GPIO6_B0_D	O		UART6_TX_M1	3.3V
164	PWM0_CH3_M2/UART3_RX_M2/I2C2_SDA_M2/GPIO6_C3_D	I/O		ETH_LED_SPD	3.3V
166	PWM0_CH0_M2/UART3_RTSN_M2/I2C2_SCL_M2/GPIO6_C0_D	I/O		USB_PWR_EN2	3.3V
168	PWM0_CH1_M2/UART3_CTSN_M2/GPIO6_C1_D	I/O		USB_PWR_EN3	3.3V
170	PWM0_CH2_M2/UART3_TX_M2/GPIO6_C2_D	I/O		ETH_LED_ACT	3.3V
172	VO_LCDC_D0/VI_CIF_D0_M1/PWM	I/O		GPIO5_A0	3.3V

	2_CH4_M0/GPIO5_A0_D				
174	VO_LCDC_D1/VI_CIF_D1_M1/PWM 2_CH5_M0/GPIO5_A1_D	I/O		GPIO5_A1	3.3V
176	VO_LCD_D2/VI_CIF_D2_M1/PWM0 _CH5_M2/GPIO5_A2_D	O		LCD RGB interface data output, MCU interfacedata input/output	3.3V
178	VO_LCD_D3/PWM0_CH4_M2/GPIO5 _A3_D	O		LCD RGB interface data output, MCU interfacedata input/output	3.3V
180	VO_LCD_D4/PWM0_CH3_M1/GPIO5 _A4_D	O		LCD RGB interface data output, MCU interfacedata input/output	3.3V
182	VO_LCD_D5/PWM0_CH2_M1/GPIO5 _A5_D	O		LCD RGB interface data output, MCU interfacedata input/output	3.3V
184	VO_LCDC_D9/VI_CIF_D5_M1/UAR T6_TX_M0/GPIO5_B1_D	I/O		SPK_EN ,Active H	3.3V
186	VO_LCD_D13/VI_CIF_D9_M1/UAR T7_RX_M0/GPIO5_B5_D	O		LCD RGB interface data output, MCU interfacedata input/output	3.3V
188	VO_LCD_D12/VI_CIF_D8_M1/UAR T7_TX_M0/GPIO5_B4_D	O		LCD RGB interface data output, MCU interfacedata input/output	3.3V
190	VO_LCD_D10/VI_CIF_D6_M1/PWM 2_CH0_M1/GPIO5_B2_D	O		LCD RGB interface data output, MCU interfacedata input/output	3.3V
192	VO_LCD_D11/VI_CIF_D7_M1/PWM 2_CH1_M1/GPIO5_B3_D	O		LCD RGB interface data output, MCU interfacedata input/output	3.3V
194	VO_LCD_D6/PWM0_CH1_M1/GPIO5 _A6_D	O		LCD RGB interface data output, MCU interfacedata input/output	3.3V
196	VO_LCD_D7/VI_CIF_D3_M1/PWM0 _CH0_M1/GPIO5_A7_D	O		LCD RGB interface data output, MCU interfacedata input/output	3.3V
198	VO_LCDC_D8/VI_CIF_D4_M1/GPI O5_B0_D	I/O		4GLET_PWR_EN	3.3V
200	VO_LCD_D14/VI_CIF_D10_M1/PW M2_CH2_M1/GPIO5_B6_D	O		LCD RGB interface data output, MCU interfacedata input/output	3.3V
202	VO_LCDC_D16/VI_CIF_D12_M1/P WM3_CH0_M1/GPIO5_C0_D	I/O		FAN Power enable ,Active H	3.3V
204	VO_LCD_D15/VI_CIF_D11_M1/PW M2_CH3_M1/GPIO5_B7_D	O		LCD RGB interface data output, MCU interfacedata input/output	3.3V
206	CAN0_RXD_M0/SPI1_CSN1_M2/UA RT3_TX_M1/GPIO5_D4_U	O		UART3_TX_M1	3.3V
208	VO_LCD_D18/VI_CIF_D14_M1/PW M3_CH2_M1/GPIO5_C2_D	O		LCD RGB interface data output, MCU interfacedata input/output	3.3V
210	VO_LCDC_D17/VI_CIF_D13_M1/P WM3_CH1_M1/GPIO5_C1_D	I/O		USB_PWR_EN4	3.3V
212	VO_LCD_D22/VI_CIF_CLKIN_M1/ PWM3_CH6_M1/GPIO5_C6_D	O		LCD RGB interface data output, MCU interfacedata input/output	3.3V
214	VO_LCD_D21/VI_CIF_CLKOUT_M1 /PWM3_CH3_M1/GPIO5_C5_D	O		LCD RGB interface data output, MCU interfacedata input/output	3.3V
216	VO_LCD_D23/VI_CIF_HSYNC_M1/ PWM3_CH7_M1/GPIO5_C7_D	O		LCD RGB interface data output, MCU interfacedata input/output	3.3V
218	CAN0_TXD_M0/PWM1_CH3_M1/UAR T3_RX_M1/GPIO5_D5_U	I		UART3_RX_M1	3.3V
220	CAN1_TXD_M0/JTAG_TMS_M2/PWM 2_CH7_M0/GPIO5_D7_U	O		CAN1_TX_M0	3.3V
222	CAN1_RXD_M0/JTAG_TCK_M2/PWM 2_CH6_M0/GPIO5_D6_U	I		CAN1_RX_M0	3.3V

224	VO_LCD_HSYNC/SPI1_CLK_M2/PWM1_CH2_M1/GPIO5_D1_D	I/O		LCD horizontal sync signal output	3.3V
226	VO_LCD_CLK/SPI1_MISO_M2/PWM1_CH0_M1/GPIO5_D3_D	I/O		LCDC pixel clk output	3.3V
228	VCCIO5	P		3.3V Output For codec,VCCIO_ACODEC Total	3.3V
230	GND	G		GND	
232	SARADC0_IN0_KEY	I		SARADC0_IN0_KEY	1.8V
234	SARADC0_IN7_BOOT	I		SARADC0_IN7_BOOT	1.8V
236	SARADC0_IN4	I		SARADC0_IN4	1.8V
238	SARADC0_IN3	I		SARADC_VIN3_HP_HOOK	1.8V
240	SARADC0_IN2	I		SARADC0_IN2	1.8V
242	SARADC0_IN1			NC	1.8V
244	SARADC0_IN6	I		SARADC0_IN6	1.8V
246	SARADC0_IN5	I		SARADC0_IN5	1.8V
248	SAI2_LRCK_M0/UART1_CTSN_M1/SPI1_CSN0_M1/GPIO3_B5_D	I/O		SAI2_LRCK_M0	1.8V
250	SAI2_SDO_M0/SPI1_MOSI_M1/PWM2_CH0_M0/GPIO3_B2_D	O		SAI2_SDO_M0	1.8V
252	SAI2_SDI_M0/SPI1_MISO_M1/PWM2_CH1_M0/GPIO3_B3_D	I		SAI2_SDI_M0	1.8V
254	SAI2_SCLK_M0/UART1_RTSN_M1/SPI1_MISO_M1/GPIO3_B4_D	I/O		SAI2_SCLK_M0	1.8V
256	VCCIO3	P		1.8V Output ,VCCIO_WIFI Max:500mA	1.8V
258	GND	G		GND	
260	GND	G		GND	

第五章 注意事项

- 接触主板时请佩戴静电手环等静电防护工具（要有良好接地）；
- 请勿带电组装、接线等操作；
- 请核对主板接口定义和外设接口定义，不能出现接错、接反；
- 请用 M3 平圆头螺丝固定主板，请勿使用沉头、规格较大的螺丝；扭螺丝时注意避免主板发生变形、弯曲；
- 注意 IO 口、串口、使能脚等电平匹配；
- 注意外接屏幕的功率，功率较大的请考虑外部供电；
- 注意产品的整体功率，选择功率足够的电源；